



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: DeWitt, Jr. et al.

§ Group Art Unit: 2192

Serial No.: 10/757,197

§ Examiner: Dam, Tuan Quang

Filed: January 14, 2004

§ Attorney Docket No.: AUS920030556US1

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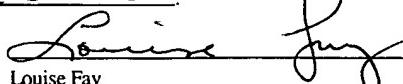
For: Method and Apparatus for  
Optimizing Code Execution Using  
Annotated Trace Information Having  
Performance Indicator and Counter  
Information

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Respectfully submitted,



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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

**DeWitt, Jr. et al.**

Serial No: **10/757,197**

Filed: **January 14, 2004**

Title: **Method and Apparatus for Optimizing  
Code Execution Using Annotated Trace  
Information Having Performance Indicator  
and Counter Information**

Group Art Unit: **2192**

Intellectual Property  
Law Department  
International Business  
Machines Corporation  
11400 Burnet Road  
Austin, Texas 78758

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

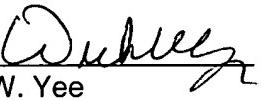
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Respectfully submitted,

  
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<p>Substitute for form 1449/PTO</p> <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p> <p>APR 25 2008</p> <p>OTPE</p>	<p>Complete if Known</p> <hr/> <table border="1"> <tr> <td>Application Number</td> <td>10/757,197</td> </tr> <tr> <td>Filing Date</td> <td>01-14-2004</td> </tr> <tr> <td>First Named Inventor</td> <td>DeWitt, Jr. et al.</td> </tr> <tr> <td>Examiner Name</td> <td>Dam, Tuan Quang</td> </tr> <tr> <td>Art Unit</td> <td>2192</td> </tr> <tr> <td>Attorney Docket No.</td> <td>AUS920030556US1</td> </tr> </table>	Application Number	10/757,197	Filing Date	01-14-2004	First Named Inventor	DeWitt, Jr. et al.	Examiner Name	Dam, Tuan Quang	Art Unit	2192	Attorney Docket No.	AUS920030556US1
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## **FOREIGN PATENT DOCUMENTS**

FOREIGN PATENT DOCUMENTS				
Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## **NON PATENT LITERATURE**

<b>Examiner Initial</b>		Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 <a href="http://en.wikipedia.org/wiki/JavaServer_Pages">http://en.wikipedia.org/wiki/JavaServer_Pages</a>
	AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
	AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from <a href="http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html">http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html</a>

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